2SC6050

Silicon NPN epitaxial planar type

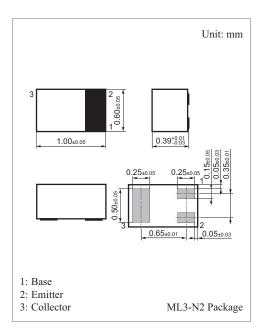
For high frequency amplification, oscillation and mixing

Features

- High transition frequency f_T
- Small collector output capacitance (Common base, input open circuited) C_{ob} and reverse transfer capacitance (Common base) C_{rb}
- Optimum for high-density mounting and downsizing of the equipment for Ultraminiature leadless package
 - $0.6 \text{ mm} \times 1.0 \text{ mm}$ (height 0.39 mm)

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	15	V	
Collector-emitter voltage (Base open)	V _{CEO}	10	V	
Emitter-base voltage (Collector open)	V _{EBO}	3	V	
Collector current	I _C	50	mA	
Collector power dissipation	P _C	100	mW	
Junction temperature	Tj	125	°C	
Storage temperature	T _{stg}	-55 to +125	°C	



Marking Symbol: 6N

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 {\rm mA}, I_{\rm B} = 0$	10			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$	3			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 10 \text{ V}, I_E = 0$			1	μΑ
Forward current transfer ratio	h _{FE}	$V_{CE} = 4 V, I_C = 5 mA$	75		400	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 20 \text{ mA}, I_{\rm B} = 4 \text{ mA}$			0.5	V
Transition frequency	f_{T}	$V_{CB} = 4 \text{ V}, I_{E} = -5 \text{ mA}, f = 200 \text{ MHz}$	1.4	1.9	2.7	GHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = 4 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$		1.4		pF
Reverse transfer capacitance (Common base)	C _{rb}	$V_{CB} = 4 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$		0.45		pF
Collector-base parameter	r _{bb'} • c _c	$V_{CB} = 4 \text{ V}, I_E = -5 \text{ mA}, f = 31.9 \text{ MHz}$		11		ps
$h_{\rm FE}$ ratio	$\Delta h_{\rm FE}$	$V_{CE} = 4 V, I_C = 100 \mu A / V_{CE} = 4 V, I_C = 5 mA$	0.75		1.6	

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

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